The Technology Sovereignty Package in Practice

Wednesday 4 November

12:00–12:45 **Registration & Networking lunch**

12:45–13:00 **Opening & Welcome**

Theatre - Main stage Talk


Kilian Gross, Director C DG Connect – Enabling & Emerging Technologies

13:00–13:35 **Silicon Sovereignty: Securing Europe's Chip Future**

Theatre - Main stage Panel


Silvana Muscella, CEO of Trust-IT, ALLPROS.eu Technical Coordinator


Oliver Schenk, Member of European Parliament


Kilian Gross, Director C DG Connect – Enabling & Emerging Technologies

13:35–14:10 **Panel “Scaling-up Europe’s fabless ecosystem"**

Theatre - Main stage Panel


Marco Ceccarelli , European Commission


Fabrizio Del Maffeo, Axelera, Chief Executive Officer


Philippe Notton, SiPearl, Chief Executive Officer


Khaled Malej, Vsora, Chief Executive Officer

14:10–14:45 **Panel “Boosting Chip Design in Europe"**

Theatre - Main stage Panel


Pierre Chastanet, Head of Unit - Microelectronics and Photonics at the European Commission


Antonio Varas, Chief Strategy Officer at Synopsys


Joe Cole, Cadence, Corporate Vice President


Paul Williamson, SVP, Corporate Ventures - Arm


Geoffrey Lee, Siemens EDA, Vice President


Gunnar Thomas, TSMC Europe, General Counsel

14:45–15:00 *Short break*

15:00–16:00 **European Chip Design Platform launch with the presence of EVP Virkkunen**

Theatre - Main stage | Keynote


Henna Virkkunen, Executive VP European Commission


Jari Kinaret, Executive Director at Chips JU


Patrick Vandenameele, CEO, imec


Romano Hoofman, Strategic Development Director at imec & EuroCDP Coordinator


Paul Williamson, Senior Vice President at ARM


Joe Cole, Corporate Vice President at Cadence


Geoffrey Lee, Vice President at Siemens EDA


Antonio Varas, Chief Strategy Officer at Synopsys


Silvana Muscella, CEO of Trust-IT, ALLPROS.eu Technical Coordinator

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16:00–16:15 **Coffee break**

16:15–18:00 **Working group: Automotive**

Paola | Parallel session

16:15–18:00 **Working group: Skills**

Elisabeth | Parallel session

16:15–18:00 **Working group: Supply Chain**

Foyer Philippe | Parallel session

16:15–18:00 **Working group: PFAS**

Marie-Henriette | Parallel session

16:15–17:00 **EuroCDP Roundtable**

Versailles room | Closed session

17:00–18:00 **Alliance Steering Committee meeting**

Louise-Marie | Closed session

18:00–20:00 **Networking cocktail**

Thursday 5 November

09:00–09:30 **Networking Coffee**

09:30–09:40 **AI-native Chip Design Challenge**

Theatre - Main stage | Talk



Katharina Rudisch, Sprind

09:40–09:50 **Forward-looking analysis on the semiconductor talent landscape to 2030**

Theatre - Main stage | Talk



Léo Saint-Martin , Associate Consultant, DECISION

09:50–10:30 **Breakthrough innovations, leadership and talent shaping Europe's semiconductor ecosystem**

Theatre - Main stage | Panel



Michael Boll, VP Public Policy Infineon



Jimena Garcia Romeu, VP of AESEMI and CEO of Alcyon Photonics



Bodil Holst, CEO of Lace Lithography



Françoise Chombar, MELEXIS Chairwoman, Pact For Skills Flanders STEM network



Erja Turunen, Executive VP, Digital technologies at VTT

10:30–10:35 **Have your say on the ECS-SRIA**

Theatre - Main stage | Talk



Caroline Bedran, Director General at Aeneas



Elisabeth Steimetz, EpoSS Director



Paolo Azzoni, Secretary General INSIDE Industry Association

10:35–10:45 **Tech trends shaping semiconductor demand**

Theatre - Main stage | Talk



Emilie Jolivet, Consulting projects director, Yole Group

10:45–11:20 **Towards physical AI**

Theatre - Main stage | Panel

 Emilie Jolivet, Consulting projects director, Yole Group

 Alberto Sevega, EssilorLuxottica, Strategic Partnerships Manager

 Stephen Marvin, Research and Development Director, PFA - Platforme automobile

 Riccardo Masucci, Managing Director EMEA Government Affairs at Intel

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11:20–11:45 **Networking Coffee & Poster session - Theatre (Exibition area)**

11:45–11:55 **A forward-looking view on the global semiconductor value chain through 2030**

Theatre - Main stage | Talk

 Giorgio Micheletti, Consulting Director, IDC

11:55–12:35 **From Technology Needs to Semiconductor Demand**

Theatre - Main stage | Panel

 Frank Bösenberg, Managing Director Silicon Saxony

 Michel Sarlotte, Department chief - Thales

 Ignacio Martín Llorente, Managing Director - Business Development at OpenNebula

 Bernard Barani, Senior Consultant - Telecom/IT

 Timo Tenhovuori, Nokia, Head of semiconductors

 Eric Eppe, Vice President, Bull Group

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12:35–12:55 **Steering Committee presentation and Q&A**

Theatre - Main stage | Talk

 Stefan Joeres, VP Business Development, Semiconductor Strategy & Strategic Projects - Robert Bosch

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12:55–13:25 **WG Recommendations to the ESB**

Theatre - Main stage | Talk

 Martina Wolfgruber, Head of EU Funding Management - Skills at Infineon Austria

 Fabrice Graignic, Europe & France Public Affairs at STMicroelectronics

 Carlos Pardo, Vicepresident at AESEMI

 Giovanni Corder, Deputy Director General | Senior Director Global Policy at ESIA

 Dimiter Prodanov, Associate Professor IICT at the Bulgarian Academy of Sciences.

 Sophie Cordeiro, Project Manager, Bosch

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13:25–13:35 **Final remarks**

Theatre - Main stage | Talk



Arian Zwegers, Deputy Head of Unit - 'Microelectronics and Photonics' at the European Commission

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13:35–14:30 **Networking Lunch and Informal Matchmaking session**
